

FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

Title of Invention	Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices																						
Application Number :																							
Date :																							
First Named Applicant:		Praveen K Samudrala																					
Attorney Docket Number:		1372.136.PRC																					
TOTAL FEE AUTHORIZED \$ 448																							
Patent fees are subject to annual revisions on or about October 1st of each year.																							
Filing as small entity																							
BASIC FILING FEE																							
<table border="1"><thead><tr><th>Fee Description</th><th>Fee Code</th><th>Amount \$</th><th>Fee Paid \$</th></tr></thead><tbody><tr><td>Utility Filing Fee</td><td>2001</td><td>385</td><td>385</td></tr><tr><td colspan="3"></td><td>Subtotal For Basic Filing Fees: \$ 385</td></tr></tbody></table>				Fee Description	Fee Code	Amount \$	Fee Paid \$	Utility Filing Fee	2001	385	385				Subtotal For Basic Filing Fees: \$ 385								
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EXTRA CLAIM FEES																							
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AUTHORIZED BILLING INFORMATION																							
The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:																							
Credit account number:		3008																					
Expiration Date (YYYYMMDD):		2004-05-31																					
Authorized name:		Anton J Hopen																					
Billing address:		33760																					